



INTRODUCTION

The goal of the Perplexus Project [1] is to develop a scalable hardware made of custom reconfigurable devices. The platform will enable the simulation of complex systems and the study of emergent behaviours in a wireless network of ubiquitous modules. These modules will act as a distributed sensor capable of computing parts of a simulation using environmental parameters.

A multi-agent platform based on the Java Agent Development Framework (JADE) was proposed to create distributed applications suiting mobile interacting nodes in an evolving environment. The software framework leverages the bio-inspired features of the underlying hardware and sustains

philogenetic, ontogenetic and epigenetic processes. DISCUSSION Figure 2 shows how agents can be implemented either in software – in Java for execution on the This poster presents the novel implementation of a **FRAMEWORK ARCHITECTURE** XScale –, in hardware – using the Ubichip assembler **NEURAL MODEL** neural system simulator on an hybrid multi-agent - or using a "software restricted" method, that is Each computational module is an hybrid system that hardware platform within the Perplexus project. The compiled into an Ubichip-executable form using a We applied the initial development tools currently consists of XScale and UbiChip processors at the proposed architecture provides a scalable and specific compiler. In the last case, the JubiCompiler available from the Project to simulate a neural flexible framework for biologically plausible neural hardware level (see below) and GNU/Linux and JADE tools disembody the agent to perform some of the network characterized by excitatory and inhibitory platforms at the software level. network modeling. computations transparently on the Ubichip (see populations of spiking neurons that are initially Figure 3), providing a unique tool to facilitate the Hardware and software platforms are structured Future work is aimed at realizing the complete connected according to probability density functions according to three axes: analysis of parallelism and synchronization on the hardware acceleration features. The intended [2]. The inhibitory population have sparse local application performance and output. implementation of the framework on mobile robots based on the evolution of species Phylogenesis connectivity, whereas the excitatory neurons have will provide a testable platform for the phylogenetic which leads to adaptation to the environment; dense local connectivity and a very sparse but flat processes. probability to send projections anywhere in the describing the development of the Ontogenesis 2 organisms during early stages; Software network. JADE / Java Agent

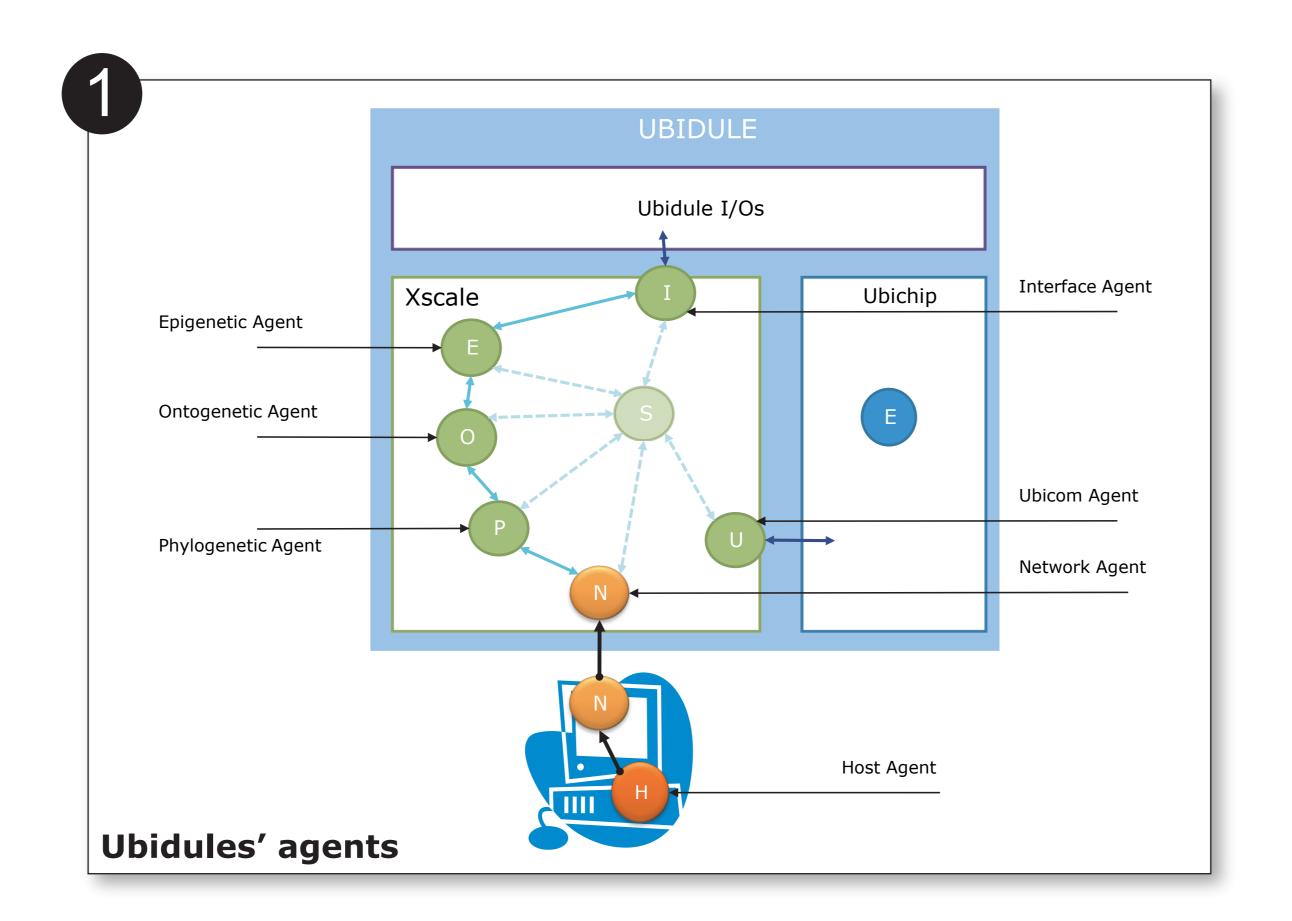
refering to adaptation, learning Epigenesis The excitatory-excitatory and excitatory-inhibitory and plasticity processes. synapses are modifiable and take discrete strength values that can change according to a spike-timing Interoperability among the hardware and the dependent plasticity (STDP) rule. We assume a software parts of the framework is provided by the background noise of 5 spikes/s during an initial use of this bio-inspired structure. developmental phase featuring cell death associated The neural network simulations are logically divided with an excessive firing rate, somewhat akin to the into appropriate software agents: P, O and E neurotoxic effect of glutamate. This phase is corresponding to the 3 axes described above. followed by the onset of STDP and by recursive Infrastructure agents are also provided to handle the spatially and temporally organized stimuli. We have input/output (I) sensors/actuators – including shown that this network dynamics is able to cameras, microphones and motors – generate detectable preferred firing sequences. communications with networks (N) and ubichips (U) and spying (S) (see Figure 1).

See poster 143.6. (poster board A39) for details

PERPLEXUS: A SIMULATION ENVIRONMENT APPLIED TO LARGE SCALE SPIKING NEURAL NETWORKS

Javier Iglesias¹, Vladyslav Shaposhnyk¹, Olivier Brousse², Thierry Gil², François Grize³, Gilles Sassatelli² and Alessandro E.P. Villa¹

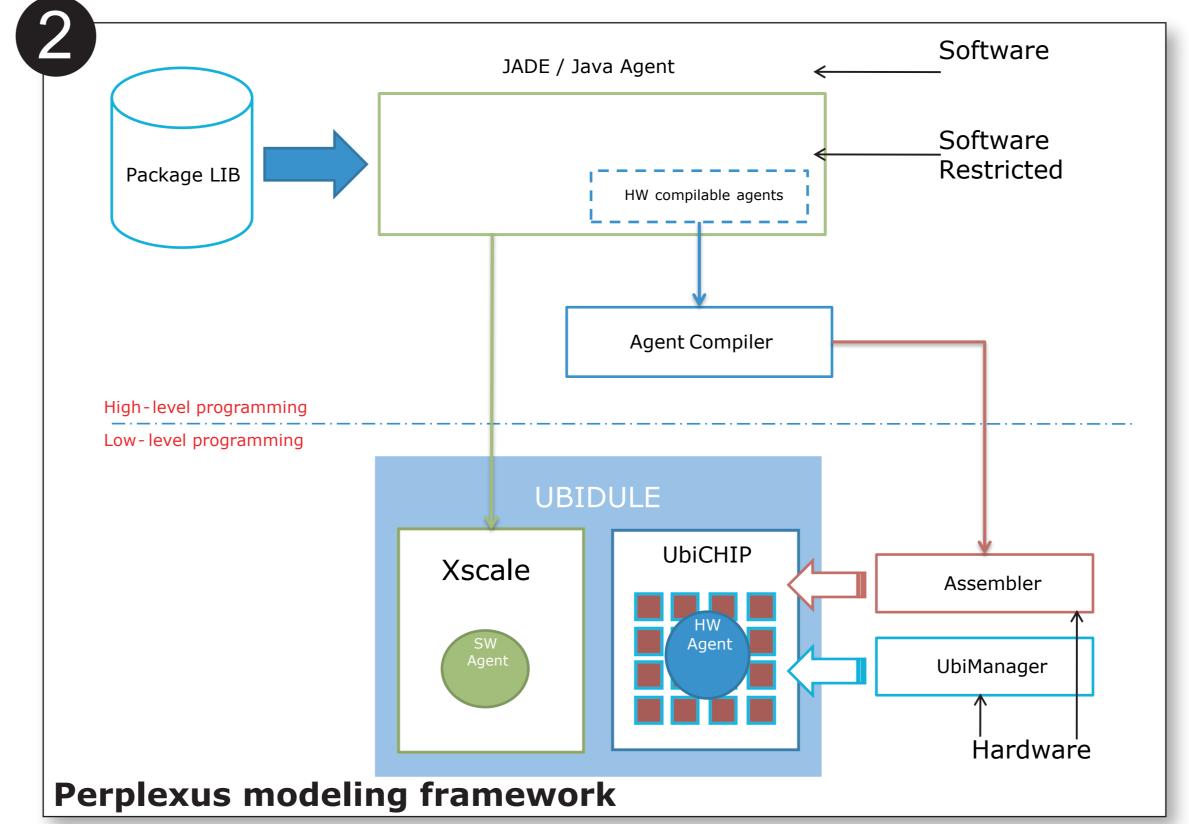
> (1) Grenoble Institute of Neuroscience, Université Jospeh Fourier, Grenoble, France; (2) Laboratoire d'Informatique, de Robotique et de Microélectronique de Montpellier, Université Monpellier 2, CNRS, Montpellier, France; (3) Institut des Systèmes d'Information, Université de Lausanne, Lausanne, Suisse

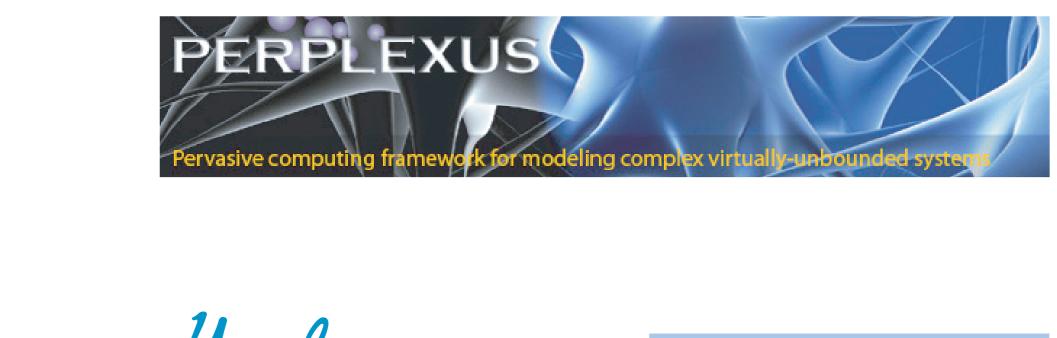


HARDWARE PLATFORM

The Ubidule is the main component of the Perplexus architecture. It features an XScale processor running the GNU/Linux operating system (OS) and allows the connection of USB devices. The programming and management of the platform is facilitated by the numerous tools available for the Free OS, while the basic features can be extended through the use of application-specific sensors and actuators.

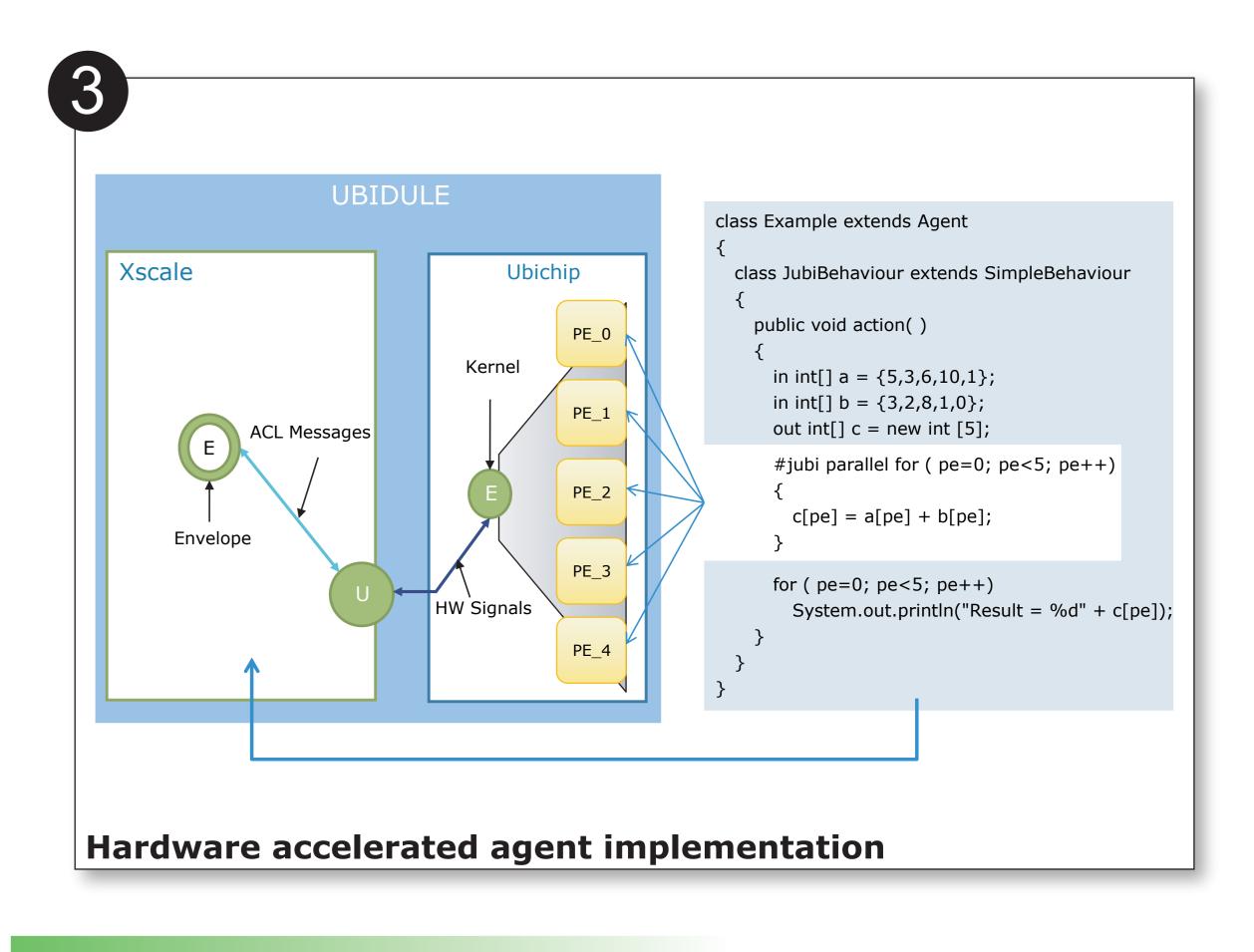
Besides the main CPU, a custom reconfigurable device named Ubichip is being developed. It can be configured either in native mode (FPGA-like) or in multiprocessor mode on which epigenetic processes, like neural network simulation, can be mapped on an array of processing elements.





Unil Iniversité de Lausann Institut des Systèmes d'Information





ACKNOWLEDGMENTS

This work is supported by the European Commission Future and Emerging Technologies Complex Systems program, grant #034632 (PERPLEXUS).

REFERENCES

http://www.perplexus.org/

[1] O. Brousse, G. Sassatelli, T. Gil, M. Robert, L. Torres, E. Sanchez, J.M. Moreno, A. Villa, H. Volken, A. Napieralski, F. Mondada (2008). Perplexus Project Programming Framework, First International Conference on Embedded Systems & Critical Applications, ICESCA'08

[2] J. Iglesias, O.K. Chibirova, A.E.P. Villa (2007). Nonlinear Dynamics Emerging in Large Scale Neural Networks with Ontogenetic and Epigenetic *Processes*, Lecture Notes in Computer Sciences 4668, 579-588